



PTO/SB/08A (07-05)
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Substitute for form 1449/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	09/887,848
		Filing Date	June 22, 2001
		First Named Inventor	Robert C. Newman, Jr.
		Art Unit	2821
		Examiner Name	Lee Wilson Tran, Thuy
Sheet 1	of 2	Attorney Docket Number	P99-16

U. S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)		Class	Subcl.	
TT		US- 6,061,259-B1	05-09-2000	DeMichele		Entire Document
TT		US- 5,568,041-B1	10-22-1996	Hesterman		Entire Document
TT		US- 5,757,166-B1	05-26-1998	Sodhi		Entire Document
TT		US- 5,185,560-B1	02-09-1993	Nilssen		Entire Document
TT		US- 5,621,279-B1	04-15-1997	Nilssen		Entire Document
TT		US- 5,434,480-B1	07-18-1995	Bobel		Entire Document
TT		US- 6,359,395-B1	03-19-2002	Quazi et al.		Entire Document
TT		US- 5,258,692-B1	11-02-1993	Jones		Entire Document
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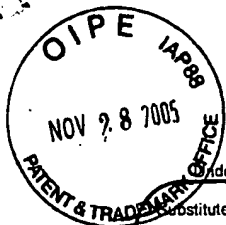
FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

Examiner Signature	/Thuy Tran/	Date Considered	01/19/2007
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Sheet 2 of 2	Attorney Docket Number	P99-16

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
TT	/	KHERALUWALA, M. H., et al., "Modified valley fill high power factor electronic ballast for compact flourescent lamps", Power Electronics Specialists Conf., 1995. PESC'95 Record, 26th Annual IEEE Atlanta, GA USA, 18-22 June 1995, New York, NY, USA, IEEE US 18, June 1995, pp. 10-14, ISBN: 0-7803-2730-6	✓
TT	/	PEPHANY, S., "MOSFET Switch Provides Efficient AC/DC Conversion", EDN Electrical Design News, Cahners Publishing Co., Newtown, MA, USA, vol. 45, no. 2, 17 February 2000, p. 149, ISSN: 0012-7515	✓
TT	/	ALONSO, J.M., et al., "Evaluation of a Novel Single-Stage High-Power-Factor Electronic Ballast Based on Integrated Buck Half-Bridge Resonant Inverter", Applied Power Electronics Conf. & Exposition, 2000. APEC 2000. 15th Annual IEEE, New Orleans, LA, USA, 6-10 Feb. 2000, Piscataway, NJ, USA, IEEE, US, 6 Feb. 2000, pp. 610-616, ISBN: 0-7803-5864-3	✓
TT	/	RIBAS, J., et al., "A New Discharge Lamp Ballast Based on a Self-Oscillating Full-Bridge Inverter Integrated with a Buck-Type PFC Circuit", APEC 2001. 16th Annual IEEE Applied Power Electronics Conf. & Exposition, Anaheim, CA, 4-8 March 2001, Annual Applied Power Electronics Conf., New York, NY: IEEE, US vol. 2 of 2. Conf. 16, 4 March 2001, pp. 688 - 694, ISBN: 0-7803-6618-2	✓

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